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Register Transfer Level (RTL) is an abstraction for defining the digital portions of a design. It is the principle abstraction used for defining electronic systems today and often serves as the golden model in the design and

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verification flow. The RTL design is usually captured using a hardware description language (HDL) such as Verilog or VHDL.

Register Transfer Level (RTL) - Semiconductor Engineering

In digital circuit design, register-transfer level (RTL) is a design abstraction which

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models a synchronous digital circuit in terms of the flow of digital signals between hardware registers, and the logical operations performed on those signals.

Register-transfer level - Wikipedia

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Digital Design with RTL ...

In the RTL Design methodology different types of registers such as Counters, Shift Register, SIPO (Serial In Parallel Out), PISO (Parallel In Serial Out) are used as the basic building blocks for any Sequential Logic Circuits.

RTL (Register Transfer Level) design

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vs Sequential logic ...

Today digital designers use hardware description languages (HDLs) to design digital systems. The most widely used HDLs are VHDL and Verilog. Both of these hardware description languages allow the user to design digital systems by writing a program that describes the behavior of the digital circuit. The

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program can then be used

Introduction to Digital Design Using Digilent FPGA Boards

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